

SYLLABUS/
CURRICULUM



PANDIAN SARASWATHI YADAV ENGINEERING COLLEGE

(Approved by AICTE & Affiliated to Anna University, Chennai)

Madurai - Sivagangai Highway, Arasanoor, Thirumansolai Post, Sivagangai Dt. - 630 561, Tamilnadu

Mobile : 9842102628, 7373002628

Email: info@psyec.edu.in

Website : www.psyec.edu.in

City Office : 10, Pandian Saraswathi St, Sivagami Nagar, Narayanapuram, Madurai - 625 014. Telefax- 0452 2682338, Mobile : 98423-02628

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

COURSE SYLLABUS

VACECE1819TST- TESSOLVE SEMICINDUCTORS IC TESTING

CHAPTER-1 Introduction to Tessolve Semiconductor 7

Overview of Tessolve Semiconductor: its history, mission, and core business areas
Understanding Tessolve's role in the semiconductor industry ecosystem
Tessolve: key challenges and objectives
Semiconductor Fundamentals Review
Understanding semiconductor manufacturing processes relevant to testing
Overview of integrated circuits (ICs) and their functionalities
Testing Methodologies

CHAPTER -2 semiconductor testing methodologies 7

Overview of wafer testing vs. package testing
Probe testing techniques and equipment
Package testing techniques and equipment
Automatic Test Equipment (ATE)
ATE programming languages and scripts (e.g., STIL, WGL)
Hands-on exercises with ATE platforms commonly used at Tessolve
Advanced Testing Techniques

CHAPTER-3 Boundary Scan Testing (JTAG) 7

Built-In Self-Test (BIST) techniques
Failure Analysis and Debugging methodologies
Reliability Testing
Overview of reliability testing standards (e.g., JEDEC)
Reliability testing techniques (e.g., HTOL, HAST, ESD)
Importance of reliability testing in semiconductor industry
Data Analysis and Interpretation

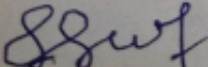
CHAPTER-4 Quality Assurance (QA) in semiconductor testing 7

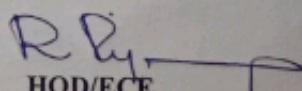
Yield analysis techniques
Failure analysis methodologies
Quality Assurance and Test Process Improvement
Implementing Six Sigma principles in testing processes
Continuous improvement methodologies
-Introduction to Design for Testability (DFT)

CHAPTER-5 DFT principles 7

DFT techniques for improving testability and reducing test costs
Collaborative approaches between design and test teams
Future Trends in Semiconductor Testing
Impact of AI/ML in testing processes
Case studies and industry examples

TOTAL 35Hrs


COURSE COORDINATOR


HOD/ECE


PRINCIPAL
PANDIAN SARASWATHI YADAV
ENGINEERING COLLEGE
Arasanoor, Thirumansolai P.O-630 561
Sivagangai Dist, Tamilnadu



PANDIAN SARASWATHI YADAV ENGINEERING COLLEGE

(Approved by AICTE & Affiliated to Anna University, Chennai)

Madurai - Sivagangai Highway, Arasanoor, Thirumansolai Post, Sivagangai Dt. - 630 561, Tamilnadu
Mobile : 9842102628, 7373002628 Email: info@psyec.edu.in Website : www.psyec.edu.in

City Office : 10, Pandian Saraswathi St, Sivagami Nagar, Narayanapuram, Madurai - 625 014. Telefax- 0452 2682338, Mobile : 98423-02628

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

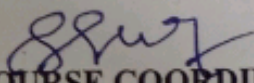
COURSE SCHEDULE

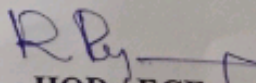
VACECE1819TST - TESSOLVE SEMICONDUCTORS IC TESTING

DATE	TIME	SYLLABUS
08.04.2019	9.00AM TO 1.00PM	Introduction to Tessolve Semiconductor Overview of Tessolve Semiconductor: its history, mission, and core business areas Understanding Tessolve's role in the semiconductor industry ecosystem Introduction to semiconductor testing at Tessolve: key challenges and objectives Semiconductor Fundamentals Review Overview of integrated circuits (ICs) and their functionalities Testing Methodologies
	2.00PM TO 5.00PM	
09.04.2019	9.00AM TO 1.00PM	Introduction to semiconductor testing methodologies Overview of wafer testing vs. package testing Probe testing techniques and equipment Package testing techniques and equipment Automatic Test Equipment (ATE) ATE programming languages and scripts (e.g., STIL, WGL) Hands-on exercises with ATE platforms commonly used at Tessolve Advanced Testing Techniques
	2.00PM TO 5.00PM	
10.04.2019	9.00AM TO 1.00PM	Boundary Scan Testing (JTAG) Built-In Self-Test (BIST) techniques Failure Analysis and Debugging methodologies Reliability Testing Reliability testing techniques (e.g., HTOL, HAST, ESD) Importance of reliability testing in semiconductor industry Data Analysis and Interpretation
	2.00PM TO 5.00PM	
11.04.2019	9.00AM TO 1.00PM	Statistical analysis of test data Yield analysis techniques NFailure analysis methodologies Quality Assurance and Test Process Improvement Implementing Six Sigma principles in testing processes Continuous improvement methodologies (e.g., Lean, Kaizen) Introduction to Design for Testability (DFT)
	2.00PM TO 5.00PM	
12.04.2019	9.00AM TO 1.00PM	Understanding DFT principles and guidelines DFT techniques for improving testability and reducing test costs Collaborative approaches between design and test teams Future Trends in Semiconductor Testing Impact of AI/ML in testing processes Case studies and industry examples
	2.00PM TO 5.00PM	

Tea Break: 10:40 am to 10:55am & 02:45 pm to 15:00 pm
Lunch Break: 12:30pm to 01:00pm

TOTAL: 35hrs


COURSE COORDINATOR


HOD / ECE


PRINCIPAL
PANDIAN SARASWATHI YADAV
ENGINEERING COLLEGE
Arasanoor, Thirumansolai P.O.-630 561
Sivagangai Dist. Tamilnadu



PANDIAN SARASWATHI YADAV ENGINEERING COLLEGE

(Approved by AICTE & Affiliated to Anna University, Chennai)

Madurai - Sivagangai Highway, Arasanoor, Thirumansolai Post, Sivagangai Dt - 630 561, Tamilnadu
Mobile : 9842102628, 7373002628 Email: info@psyec.edu.in Website : www.psyec.edu.in

City Office : 10, Pandian Saraswathi St, Sivagami Nagar, Narayanapuram, Madurai - 625 014. Telefax- 0452 2682338, Mobile : 98423-02628

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Academic Year 2018-2019

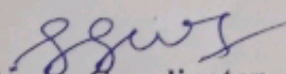
One page Report

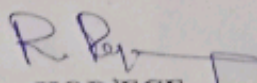
Name of the course : TESSOLVE SEMICINDUCTORS IC TESTING
Course Code : VACECE1819TST
Resource Person : Dr R.RAJA ASP/ECE-PSYEC
Course Coordinator : MS P.PADMASRI AP/ECE
Date/Duration : 08.04.2019-12.04.2019— 35 hours

I here affirm that the Third-year students of strength 26 have been taught the value-added course title "TESSOLVE SEMICINDUCTORS IC TESTING" as per the syllabus and completed within the stipulated time duration.

I confirm that the value-added course titled "TESSOLVE SEMICINDUCTORS IC TESTING" has been conducted in the beginning of the semester and course delivery along with the attendance of the students was recorded.

I confirmed that all the students were actively participated in the course and the eligible students were certified for the course.


Course Coordinator


HOD/ECE


PRINCIPAL

PRINCIPAL
PANDIAN SARASWATHI YADAV
ENGINEERING COLLEGE
Arasanoor, Thirumansolai P.O-630 561
Sivagangai Dist. Tamilnadu

ASSESSMENT PROCEDURE



PANDIAN SARASWATHI YADAV ENGINEERING COLLEGE

(Approved by AICTE & Affiliated to Anna University, Chennai)

Madurai - Sivagangai Highway, Arasanoor, Thirumansolai Post, Sivagangai Dt. - 630 561, Tamilnadu
Mobile : 9842102628, 7373002628 Email: info@psyec.edu.in Website : www.psyec.edu.in

City Office : 10, Pandian Saraswathi St, Sivagami Nagar, Narayanapuram, Madurai - 625 014. Telefax- 0452 2682338, Mobile : 98423-02628

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TESSOLVE SEMICINDUCTORS IC TESTING-VACECE1819TST

ASSESSMENT QUESTION WITH ANSWER

1. What is the basic principle behind the operation of a semiconductor diode?

- a) Ohm's Law
- b) Electron tunneling
- c) PN junction
- d) Photoelectric effect

Answer: c) PN junction

2. Which of the following is NOT a common semiconductor material used in the semiconductor industry?

- a) Silicon
- b) Germanium
- c) Gallium arsenide
- d) Aluminum


Answer: d) Aluminum

3. What does ATE stand for in the context of semiconductor testing?

- a) Automated Test Equipment
- b) Advanced Test Execution
- c) Analog Test Engineering
- d) Assembly Test Environment

Answer: a) Automated Test Equipment

4. Which semiconductor testing technique is commonly used for testing digital integrated circuits (ICs) with boundary scan capability?


PRINCIPAL
PANDIAN SARASWATHI YADAV
ENGINEERING COLLEGE
Arasanoor, Thirumansolai Post,
Sivagangai Dist. Tamilnadu

- a) Scan testing
- b) Probe testing
- c) Burn-in testing
- d) Functional testing

Answer: a) Scan testing

5. What does DFT stand for in the context of semiconductor testing?

- a) Digital Frequency Testing
- b) Design for Testability
- c) Device Function Test
- d) Dynamic Fault Tolerance

Answer: b) Design for Testability

6. Which reliability testing technique subjects semiconductor devices to elevated temperatures and voltages to accelerate failure mechanisms?

- a) High Voltage Testing
- b) Burn-in Testing
- c) ESD Testing
- d) HALT Testing

Answer: b) Burn-in Testing

7. What is the primary purpose of Built-In Self-Test (BIST) in semiconductor devices?

- a) To improve manufacturing yield
- b) To reduce test time
- c) To eliminate the need for external testing equipment
- d) To detect faults within the device itself during operation

Answer: d) To detect faults within the device itself during operation

8. What type of testing is typically performed at the wafer level before individual semiconductor devices are packaged?

- a) Final testing
- b) System testing

c) Probe testing

d) Functional testing

Answer: c) Probe testing

9. Which statistical analysis technique is commonly used in semiconductor testing to assess manufacturing yield?

a) ANOVA (Analysis of Variance)

b) Regression analysis

c) Six Sigma

d) Pareto analysis

Answer: a) ANOVA (Analysis of Variance)

10. What is the significance of JTAG (Joint Test Action Group) in semiconductor testing?

a) It is a standard for boundary scan testing

b) It is a programming language used in ATE

c) It is a reliability testing standard

d) It is a technique for wafer-level testing

Answer: a) It is a standard for boundary scan testing

PRINCIPAL
PANDIAN SARASWATHI YADAV
ENGINEERING COLLEGE
Arasankottai, Thanjavur P.O.-630 561
Sivagangal Dist. Tamilnadu



PANDIAN SARASWATHI YADAV ENGINEERING COLLEGE

(Approved by AICTE & Affiliated to Anna University, Chennai)

Madurai - Sivagangai Highway, Arasanoor, Thirumansolai Post, Sivagangai Dt. - 630 561, Tamilnadu
Mobile : 9842102628, 7373002628 Email: info@psyec.edu.in Website : www.psyec.edu.in

City Office : 10, Pandian Saraswathi St, Sivagami Nagar, Narayaniapuram, Madurai - 625 014. Telefax- 0452 2682338, Mobile : 98423-02628

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TESSOLVE SEMICINDUCTORS IC TESTING - VACECE1819TST

ASSESSMENT TEST PAPER

REGISTER NO: 91206106003

NAME OF THE STUDENT: Megumithurai 8

YEAR/SEM: IV / VI

8
10

1. What is the basic principle behind the operation of a semiconductor diode?

- a) Ohm's Law
- b) Electron tunneling
- c) PN junction
- d) Photoelectric effect

2. Which of the following is NOT a common semiconductor material used in the semiconductor industry?

- a) Silicon
- b) Germanium
- c) Gallium arsenide
- d) Aluminum

3. What does ATE stand for in the context of semiconductor testing?

- a) Automated Test Equipment
- b) Advanced Test Execution
- c) Analog Test Engineering
- d) Assembly Test Environment

PRINCIPAL
PANDIAN SARASWATHI YADAV
ENGINEERING COLLEGE
Arasanoor, Thirumansolai P.O. - 630 561
Sivagangai Dist. Tamilnadu

4. Which semiconductor testing technique is commonly used for testing digital integrated circuits (ICs) with boundary scan capability?

- a) Scan testing
- b) Probe testing
- c) Burn-in testing
- d) Functional testing

5. What does DFT stand for in the context of semiconductor testing?

- a) Digital Frequency Testing
- b) Design for Testability
- c) Device Function Test
- d) Dynamic Fault Tolerance

6. Which reliability testing technique subjects semiconductor devices to elevated temperatures and voltages to accelerate failure mechanisms?

- a) High Voltage Testing
- b) Burn-in Testing
- c) ESD Testing
- d) HALT Testing

7. What is the primary purpose of Built-In Self-Test (BIST) in semiconductor devices?

- a) To improve manufacturing yield
- b) To reduce test time
- c) To eliminate the need for external testing equipment
- d) To detect faults within the device itself during operation

8. What type of testing is typically performed at the wafer level before individual semiconductor devices are packaged?

- a) Final testing
- b) System testing
- c) Probe testing

d) Functional testing

9. Which statistical analysis technique is commonly used in semiconductor testing to assess manufacturing yield?

a) ANOVA (Analysis of Variance)

b) Regression analysis

c) Six Sigma

d) Pareto analysis


10. What is the significance of JTAG (Joint Test Action Group) in semiconductor testing?

a) It is a standard for boundary scan testing

b) It is a programming language used in ATE

c) It is a reliability testing standard

d) It is a technique for wafer-level testing


PRINCIPAL
PANDIAN SARASWATHI YADAV
ENGINEERING COLLEGE
Araseneer, Thirumansolai P.O-630 561
Sivagangal Dist. Tamilnadu

Pandian Saraswathi yadav Engineering College, Arasanoor -630561

Department of Electronics and Communication Engineering
Student Performance Sheet

Academic Year : 2018-2019

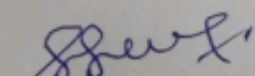
Course Code : VACECE1819TST

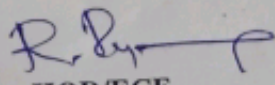
Course Name : TESSOLVE SEMICONDUCTORS IC TESTING


Duration of hours : 35

Period of Course : 08.04.2019-12.04.2019

Assessment marks			Marks
Sl. No	Register Number	Student Name	
1	912016106001	AJAY KUMAR	72
2	912016106002	AJITTHA	84
3	912016106003	ALAGUMUTHUMARI	94
4	912016106004	BALA DURGA	95
5	912016106005	BHAVADHARANI	96
6	912016106006	EBIN ROSE CHRISTINA	100
7	912016106007	ELAKKIYA	99
8	912016106008	KARTHIKA	84
9	912016106009	KARTHIKEYAN	76
10	912016106010	KISHORE	80
11	912016106012	MAHALAKSHMI	88
12	912016106013	MALLIKHAA EASWARI	94
13	912016106014	MANOJ KANNAN	71
14	912016106015	MONISHA	89
15	912016106016	MUTHU SELVI	92
16	912016106017	NAGA SOUNDARI	94
17	912016106018	NANDHINI	89
18	912016106020	PRAVEENA	90
19	912016106023	SANKARALINGAM	84
20	912016106026	SONAIPANDI	76
21	912016106029	VIJAYALAKSHMI	81
22	912016106031	YAMINI	79
23	912016106501	THANUJA PARAMESWARI B.M	80
24	912016106701	DEVI UMA MAGASHWARI B	99
25	912016106901	RISHIKESH KRISHANA M	70


Course coordinator


HOD/ECE


PRINCIPAL
PANDIAN SARASWATHI YADAV
ENGINEERING COLLEGE
Arasanoor, Thiruvananthapuram P.O-630 561
Sivagangai Dist. Tamilnadu

Enrollment
Student Name
List

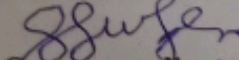
Department of Electronics and Communication Engineering
Student Registration Sheet

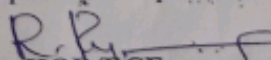
Academic Year : 2018-2019
Course Code : VACECE1819TST
Course Name : TESSOLVE SEMICONDUCTORS IC TESTING
Duration of hours : 35
Period of Course : 08.04.2019-12.04.2019


Enrolled Students List			Signature
Sl. No	Register Number	Student Name	
1	912016106001	AJAY KUMAR	Ajay Kumar
2	912016106002	AJITHA	Ajitha
3	912016106003	ALAGUMUTHUMARI	Alagumuthumari
4	912016106004	BALA DURGA	Bala Durga
5	912016106005	BHAVADHARANI	Bhavadharani
6	912016106006	EBIN ROSE CHRISTINA	Ebin Rose Christina
7	912016106007	ELAKKIYA	Elakkiya
8	912016106008	KARTHIKA	Karthika
9	912016106009	KARTHIKEYAN	Karthikeyan
10	912016106010	KISHORE	Kishore
11	912016106012	MAHALAKSHMI	Mahalakshmi
12	912016106013	MALLIKHAA EASWARI	Mallikhaa Easwari
13	912016106014	MANOJ KANNAN	Manoj Kannan
14	912016106015	MONISHA	Monisha
15	912016106016	MUTHU SELVI	Muthu Selvi
16	912016106017	NAGA SOUNDARI	Naga Soundari
17	912016106018	NANDHINI	Nandhini
18	912016106020	PRAVEENA	Praveena
19	912016106023	SANKARALINGAM	Sankaralingam
20	912016106026	SONAIPANDI	Sonaipandi
21	912016106029	VIJAYALAKSHMI	Vijayalakshmi
22	912016106031	YAMINI	Yamini
23	912016106501	THANUJA PARAMESWARI B.M	Thanuja Parameswari B.M
24	912016106701	DEVI UMA MAGASHWARI B	Devi Uma Magashwari B
25	912016106901	RISHIKESH KRISHANA M	Rishikesh Krishana M

Tea Break FN- 11:00 am to 11:15am & AN-03:00 pm to 03:15 pm

Lunch Break 12:15 pm to 01:00pm


Course coordinator


HOD/ECE


PRINCIPAL

PRINCIPAL
PANDIAN SARASWATHI YADAV
ENGINEERING COLLEGE
Arasanoor, Thirumansolai P.O-630 561
Sivagangai Dist. Tamilnadu

STUDENTS ATTENDANCE

Student Attendance Sheet

Academic Year : 2018-2019
 Course Code : VACECE1819TST
 Course Name : TESSOLVE SEMICONDUCTORS IC TESTING
 Duration of hours : 35
 Period of Course : 08.04.2019-12.04.2019

Attendance Sheet			Date: 8/4/19					
Sl. No	Register Number	Student Name	09.00 am - 10.00 am	10.00 am - 11.00 am	11.15 am - 12.15 pm	01.00 pm - 02.00 pm	02.00 pm - 03.00 pm	03.15 pm - 04.15 pm
1	912016106001	AJAY KUMAR*	Ajay	Ajay	Ajay	Ajay	Ajay	Ajay
2	912016106002	AJITHA	Ajitha	Ajitha	Ajitha	Ajitha	Ajitha	Ajitha
3	912016106003	ALAGUMUTHUMARI	Ast	Ast	Ast	Ast	Ast	Ast
4	912016106004	BALA DURGA	Bd	Bd	Bd	Bd	Bd	Bd
5	912016106005	BHAVADHARANI	Bhavath	Bhavath	Bhavath	Bhavath	Bhavath	Bhavath
6	912016106006	EBIN ROSE CHRISTINA	Ebsc	Ebsc	Ebsc	Ebsc	Ebsc	Ebsc
7	912016106007	ELAKKIYA	Elakiya	Elakiya	Elakiya	Elakiya	Elakiya	Elakiya
8	912016106008	KARTHIKA	Kar	Kar	Kar	Kar	Kar	Kar
9	912016106009	KARTHIKEYAN	Kart	Kart	Kart	Kart	Kart	Kart
10	912016106010	KISHORE	Kish	Kish	Kish	Kish	Kish	Kish
11	912016106012	MAHALAKSHMI	Mah	Mah	Mah	Mah	Mah	Mah
12	912016106013	MALLIKHAA EASWARI	Mall	Mall	Mall	Mall	Mall	Mall
13	912016106014	MANOJ KANNAN	Man	Man	Man	Man	Man	Man
14	912016106015	MONISHA *	Mon	Mon	Mon	Mon	Mon	Mon
15	912016106016	MUTHU SELVI	Muth	Muth	Muth	Muth	Muth	Muth
16	912016106017	NAGA SOUNDARI	Naga	Naga	Naga	Naga	Naga	Naga
17	912016106018	NANDHINI	Nan	Nan	Nan	Nan	Nan	Nan
18	912016106020	PRAVEENA	Prave	Prave	Prave	Prave	Prave	Prave
19	912016106023	SANKARALINGAM	Sank	Sank	Sank	Sank	Sank	Sank
20	912016106026	SONAIPANDI	Sona	Sona	Sona	Sona	Sona	Sona
21	912016106029	VIJAYALAKSHMI	Vil	Vil	Vil	Vil	Vil	Vil
22	912016106031	YAMINI	Yam	Yam	Yam	Yam	Yam	Yam
23	912016106501	THANUJA PARAMESWARI B.M	Tha	Tha	Tha	Tha	Tha	Tha
24	912016106701	DEVI UMA MAGASHWARI B	B. Dev	B. Dev	B. Dev	B. Dev	B. Dev	B. Dev
25	912016106901	RISHIKESH KRISHANA M	M. Rish	M. Rish	M. Rish	M. Rish	M. Rish	M. Rish

Tea Break FN- 11:00 am to 11:15am & AN-03:00 pm to 03:15 pm
 Lunch Break 12:15 pm to 01:00pm.*

[Signature]
 Course coordinator

[Signature]
 HOD/ECE

[Signature]
 PRINCIPAL
 PANDIAN SARASWATHI YADAV
 ENGINEERING COLLEGE
 Arasanoor, Thirumansolai P.O-630 561

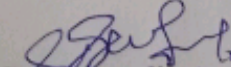
Pandian Saraswathi yadav Engineering College, Arasanoor -630561
Department of Electronics and Communication Engineering

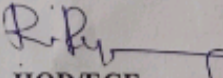
Student Attendance Sheet

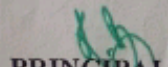
Academic Year : 2018-2019
 Course Code : VACECE1819TST
 Course Name : TESSOLVE SEMICONDUCTORS IC TESTING
 Duration of hours : 35
 Period of Course : 08.04.2019-12.04.2019

Attendance Sheet			Date: 9/4/19					
Sl. No	Register Number	Student Name	09.00 am - 10.00 am	10.00 am - 11.00 am	11.15 am - 12.15pm	01.00 pm - 02.00 pm	02.00 pm - 03.00 pm	03.15 pm - 04.15 pm
1	912016106001	AJAY KUMAR	Ack	Ab	Ab	Ab	Ab	Ab
2	912016106002	AJITTHA	Ajittha	Ajittha	Ajittha	Ajittha	Ajittha	Ajittha
3	912016106003	ALAGUMUTHUMARI	Alu	Alu	Alu	Alu	Alu	Alu
4	912016106004	BALA DURGA	Bala	Bala	Bala	Bala	Bala	Bala
5	912016106005	BHAVADHARANI	Bhava	Bhava	Bhava	Bhava	Bhava	Bhava
6	912016106006	EBIN ROSE CHRISTINA	Ebs	Ebs	Ebs	Ebs	Ebs	Ebs
7	912016106007	ELAKKIYA	Ele	Ele	Ele	Ele	Ele	Ele
8	912016106008	KARTHIKA	Kee	Kee	Kee	Kee	Kee	Kee
9	912016106009	KARTHIKEYAN	K	K	K	K	K	K
10	912016106010	KISHORE	Kis	Kis	Kis	Kis	Kis	Kis
11	912016106012	MAHALAKSHMI	Maha	Maha	Maha	Maha	Maha	Maha
12	912016106013	MALLIKHAA EASWARI	Malli	Malli	Malli	Malli	Malli	Malli
13	912016106014	MANOJ KANNAN	Manoj	Manoj	Manoj	Manoj	Manoj	Manoj
14	912016106015	MONISHA	Monisha	Monisha	Monisha	Monisha	Monisha	Monisha
15	912016106016	MUTHU SELVI	Muthu	Muthu	Muthu	Muthu	Muthu	Muthu
16	912016106017	NAGA SOUNDARI	N	N	N	N	N	N
17	912016106018	NANDHINI	Nand	Nand	Nand	Nand	Nand	Nand
18	912016106020	PRAVEENA	Prave	Prave	Prave	Prave	Prave	Prave
19	912016106023	SANKARALINGAM	Sank	Sank	Sank	Sank	Sank	Sank
20	912016106026	SONAIPANDI	AB	AB	AB	AB	AB	AB
21	912016106029	VIJAYALAKSHMI	Vij	Vij	Vij	Vij	Vij	Vij
22	912016106031	YAMINI	Yamini	Yamini	Yamini	Yamini	Yamini	Yamini
23	912016106501	THANUJA PARAMESWARI B.M	AB	AB	AB	AB	AB	AB
24	912016106701	DEVI UMA MAGASHWARI B	Dev	Dev	Dev	Dev	Dev	Dev
25	912016106901	RISHIKESH KRISHANA M	R	R	R	R	R	R

Tea Break FN- 11:00 am to 11:15am & AN-03:00 pm to 03:15 pm
 Lunch Break 12:15 pm to 01:00pm


 Course coordinator


 HOD/ECE


 PRINCIPAL

PRINCIPAL
 PANDIAN SARASWATHI YADAV
 ENGINEERING COLLEGE
 Arasanoor, Thirumangalai P.O-630 561
 Sivagangai Dist. Tamilnadu

Pandian Saraswathi yaday Engineering College, Arasanoor -630561
Department of Electronics and Communication Engineering

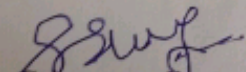
Student Attendance Sheet

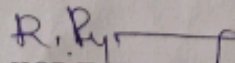
Academic Year : 2018-2019
 Course Code : VACECE1819TST
 Course Name : TESSOLVE SEMICONDUCTORS IC TESTING
 Duration of hours : 35
 Period of Course : 08.04.2019-12.04.2019

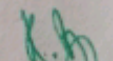
Attendance Sheet			Date: 10/4/19					
Sl. No	Register Number	Student Name	09.00 am - 10.00 am	10.00 am - 11.00 am	11.15 am - 12.15pm	01.00 pm - 02.00 pm	02.00 pm - 03.00 pm	03.15 pm - 04.15 pm
1	912016106001	AJAY KUMAR	Ajaykumar	Ajaykumar	Ajaykumar	Ajaykumar	Ajaykumar	Ajaykumar
2	912016106002	AJITHA	Ajitha	Ajitha	Ajitha	Ajitha	Ajitha	Ajitha
3	912016106003	ALAGUMUTHUMARI	Alagu	Alagu	Alagu	Alagu	Alagu	Alagu
4	912016106004	BALA DURGA	Baladurga	Baladurga	Baladurga	Baladurga	Baladurga	Baladurga
5	912016106005	BHAVADHARANI	Bhas	Bhas	Bhas	Bhas	Bhas	Bhas
6	912016106006	EBIN ROSE CHRISTINA	eh	eh	eh	eh	eh	eh
7	912016106007	ELAKKIYA	(Ele)	(Ele)	(Ele)	(Ele)	(Ele)	(Ele)
8	912016106008	KARTHIKA	Ar	Ar	Ar	Ar	Ar	Ar
9	912016106009	KARTHIKEYAN	Ar	Ar	Ar	Ar	Ar	Ar
10	912016106010	KISHORE	Kis	Kis	Kis	Kis	Kis	Kis
11	912016106012	MAHALAKSHMI	maha	maha	maha	maha	maha	maha
12	912016106013	MALLIKHAA EASWARI	malls	malls	malls	malls	malls	malls
13	912016106014	MANOJ KANNAN	manoj	manoj	manoj	manoj	manoj	manoj
14	912016106015	MONISHA	mon	mon	mon	mon	mon	mon
15	912016106016	MUTHU SELVI	Muthselvi	Muthselvi	Muthselvi	Muthselvi	Muthselvi	Muthselvi
16	912016106017	NAGA SOUNDARI	N	N	N	N	N	N
17	912016106018	NANDINI	Nand	Nand	Nand	Nand	Nand	Nand
18	912016106020	PRAVEENA	Pra	Pra	Pra	Pra	Pra	Pra
19	912016106023	SANKARALINGAM	Sank	Sank	Sank	Sank	Sank	Sank
20	912016106026	SONAIPANDI	Sona	Sona	Sona	Sona	Sona	Sona
21	912016106029	VIJAYALAKSHMI	Vij	Vij	Vij	Vij	Vij	Vij
22	912016106031	YAMINI	Yamini	Yamini	Yamini	Yamini	Yamini	Yamini
23	912016106501	THANUJA PARAMESWARI B.M	AD	AB	AB	AD	AB	AB
24	912016106701	DEVI UMA MAGASHWARI B	Dumsh	Dumsh	Dumsh	Dumsh	Dumsh	Dumsh
25	912016106901	RISHIKESH KRISHANA M	R	R	R	R	R	R

Tea Break FN- 11:00 am to 11:15am & AN-03:00 pm to 03:15 pm

Lunch Break 12:15 pm to 01:00pm


Course coordinator


HOD/ECE


PRINCIPAL
PRINCIPAL
 PANDIAN SARASWATHI YADAV
 ENGINEERING COLLEGE
 Arasanoor, Thirumansolai P.O-630 561
 Chengam Dist. Tamilnadu

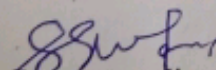
Student Attendance Sheet

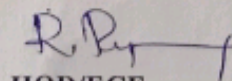
Academic Year : 2018-2019
Course Code : VACECE1819TST
Course Name : TESSOLVE SEMICONDUCTORS IC TESTING
Duration of hours : 35
Period of Course : 08.04.2019-12.04.2019

Attendance Sheet			Date: 11/4/19					
Sl. No	Register Number	Student Name	09.00 am - 10.00 am	10.00 am - 11.00 am	11.15 am - 12.15pm	01.00 pm - 02.00 pm	02.00 pm - 03.00 pm	03.15 pm - 04.15 pm
1	912016106001	AJAY KUMAR	Ake	Ake	Ake	Ake	Ake	Ake
2	912016106002	AJITHA	Ajitha	Ajitha	Ajitha	Ajitha	Ajitha	Ajitha
3	912016106003	ALAGUMUTHUMARI	Alagu	Alagu	Alagu	Alagu	Alagu	Alagu
4	912016106004	BALA DURGA	Bala	Bala	Bala	Bala	Bala	Bala
5	912016106005	BHAVADHARANI	Bha	Bha	Bha	Bha	Bha	Bha
6	912016106006	EBIN ROSE CHRISTINA	elb	elb	elb	elb	elb	elb
7	912016106007	ELAKKIYA	Ele	Ele	Ele	Ele	Ele	Ele
8	912016106008	KARTHIKA	Kar	Kar	Kar	Kar	Kar	Kar
9	912016106009	KARTHIKEYAN	K	K	K	K	K	K
10	912016106010	KISHORE	Kish	Kish	Kish	Kish	Kish	Kish
11	912016106012	MAHALAKSHMI	maha	maha	maha	maha	maha	maha
12	912016106013	MALLIKHAA EASWARI	mali	mali	mali	mali	mali	mali
13	912016106014	MANOJ KANNAN	manoj	manoj	manoj	manoj	manoj	manoj
14	912016106015	MONISHA	mai	mai	mai	mai	mai	mai
15	912016106016	MUTHU SELVI	Muthu	Muthu	Muthu	Muthu	Muthu	Muthu
16	912016106017	NAGA SOUNDARI	N	N	N	N	N	N
17	912016106018	NANDHINI	Nand	Nand	Nand	Nand	Nand	Nand
18	912016106020	PRAVEENA	Pra	Pra	Pra	Pra	Pra	Pra
19	912016106023	SANKARALINGAM	Sank	Sank	Sank	Sank	Sank	Sank
20	912016106026	SONAIPANDI	AB	AB	AB	AB	AB	AB
21	912016106029	VIJAYALAKSHMI	Viji	Viji	Viji	Viji	Viji	Viji
22	912016106031	YAMINI	Yami	Yami	Yami	Yami	Yami	Yami
23	912016106501	THANUJA PARAMESWARI B.M.	AB	AB	AB	AB	AB	AB
24	912016106701	DEVI UMA MAGASHWARI B	Dubha	Dubha	Dubha	Dubha	Dubha	Dubha
25	912016106901	RISHIKESH KRISHANA M	Ra	Ra	Ra	Ra	Ra	Ra

Tea Break FN- 11:00 am to 11:15am & AN-03:00 pm to 03:15 pm

Lunch Break 12:15 pm to 01:00pm


Course coordinator


HOD/ECE


PRINCIPAL

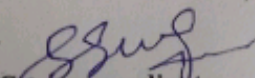
PRINCIPAL
PANDIAN SARASWATHI YADAV
ENGINEERING COLLEGE
Arasanoor, Thirumansolai P.O-630 56
Sivagangai Dist. Tamilnadu

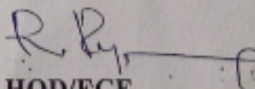
Pandian Saraswathi yadav Engineering College, Arasanoor -630561
 Department of Electronics and Communication Engineering
Student Attendance Sheet


Academic Year : 2018-2019
 Course Code : VACECE1819TST
 Course Name : TESSOLVE SEMICONDUCTORS IC TESTING
 Duration of hours : 35
 Period of Course : 08.04.2019-12.04.2019

Attendance Sheet			Date: 12/4/19					
SL No	Register Number	Student Name	09.00 am - 10.00 am	10.00 am - 11.00 am	11.15 am - 12.15pm	01.00 pm - 02.00 pm	02.00 pm - 03.00 pm	03.15 pm - 04.15 pm
1	912016106001	AJAY KUMAR	Ajaykumar	Ajaykumar	Ajaykumar	Ajaykumar	Ajaykumar	Ajaykumar
2	912016106002	AJITHA	Ajitha	Ajitha	Ajitha	Ajitha	Ajitha	Ajitha
3	912016106003	ALAGUMUTHUMARI	Alagu	Alagu	Alagu	Alagu	Alagu	Alagu
4	912016106004	BALA DURGA	Baladurga	Baladurga	Baladurga	Baladurga	Baladurga	Baladurga
5	912016106005	BHAVADHARANI	Bha	Bha	Bha	Bha	Bha	Bha
6	912016106006	EBIN ROSE CHRISTINA	elb	elb	elb	elb	elb	elb
7	912016106007	ELAKKIYA	(El)	(El)	(El)	(El)	(El)	(El)
8	912016106008	KARTHIKA	ka	ka	ka	ka	ka	ka
9	912016106009	KARTHIKEYAN	(K)	(K)	(K)	(K)	(K)	(K)
10	912016106010	KISHORE	ka	ka	ka	ka	ka	ka
11	912016106012	MAHALAKSHMI	maha	maha	maha	maha	maha	maha
12	912016106013	MALLIKHAA EASWARI	malha	malha	malha	malha	malha	malha
13	912016106014	MANOJ KANNAN	manj	manj	manj	manj	manj	manj
14	912016106015	MONISHA	mon	mon	mon	mon	mon	mon
15	912016106016	MUTHU SELVI	MuthuSelvi	MuthuSelvi	MuthuSelvi	MuthuSelvi	MuthuSelvi	MuthuSelvi
16	912016106017	NAGA SOUNDARI	(N)	(N)	(N)	(N)	(N)	(N)
17	912016106018	NANDHINI	Nand	Nand	Nand	Nand	Nand	Nand
18	912016106020	PRAVEENA	Pr	Pr	Pr	Pr	Pr	Pr
19	912016106023	SANKARALINGAM	Sank	Sank	Sank	Sank	Sank	Sank
20	912016106026	SONAIPANDI	Sona	Sona	Sona	Sona	Sona	Sona
21	912016106029	VIJAYALAKSHMI	Vij	Vij	Vij	Vij	Vij	Vij
22	912016106031	YAMINI	Yamini	Yamini	Yamini	Yamini	Yamini	Yamini
23	912016106501	THANUJA PARAMESWARI B.M	Ar	Ar	Ar	Ar	Ar	Ar
24	912016106701	DEVI UMA MAGASHWARI B	Dul	Dul	Dul	Dul	Dul	Dul
25	912016106901	RISHIKESH KRISHANA M	Ra	Ra	Ra	Ra	Ra	Ra

Tea Break FN- 11:00 am to 11:15am & AN-03:00 pm to 03:15 pm
 Lunch Break 12:15 pm to 01:00pm


 Course coordinator


 HOD/ECE


 PRINCIPAL

PRINCIPAL
 PANDIAN SARASWATHI YADAV
 ENGINEERING COLLEGE
 Arasanoor, Thirumansolai P.O-630 561
 Sivagangai Dist. Tamilnadu

MODEL
CERTIFICATES

PANDIAN SARASWATHI YADAV ENGINEERING COLLEGE

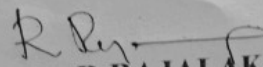
ARASANOOR, SIVAGANGAI-MADURAI HIGHWAYS TAMIL NADU-630561

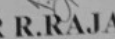
CERTIFICATE OF COMPLETION

This certificate is proudly presented to

Mr. Karthikeyan

Has Successfully Completed the Value Added Certified
Course on TESSOLVE SEMICONDUCTORS IC
TESTING During 08.04.2019 to 12.04.2019


Mrs. R. RAJALAKSHMI AP/ECE
HOD/ECE


DR R. RAJA
PRINCIPAL
PANDIAN SARASWATHI
ENGINEERING COLLEGE
Arasanoor, Thirumansolai P.O. 630 561
Tamil Nadu

Digitally signed by
R RAJA
R RAJA
Date: 2024.06.27
10:40:13 +05'30'

PANDIAN SARASWATHI YADAV ENGINEERING COLLEGE

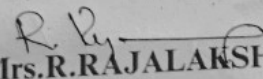
ARASANOOR, SIVAGANGAI-MADURAI HIGHWAYS TAMIL NADU-630561

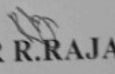
CERTIFICATE OF COMPLETION

This certificate is proudly presented to

Ms. BALADURGA

Has Successfully Completed the Value Added Certified
Course on TESSOLVE SEMICONDUCTORS IC
TESTING During 08.04.2019 to 12.04.2019


Mrs. R. RAJALAKSHMI AP/ECE
HOD/ECE


PRINCIPAL DR R. RAJA
PANDIAN SARASWATHI YADAV
ENGINEERING COLLEGE
Arasanoor, Thirumangaludi - 630 561
Sivagangai Dist. Tamil Nadu

R RAJA Digitally signed by R RAJA
Date: 2024.06.27 10:40:25
+05'30'

PANDIAN SARASWATHI YADAV ENGINEERING COLLEGE

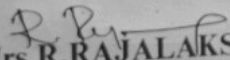
ARASANOOR, SIVAGANGAI-MADURAI HIGHWAYS TAMIL NADU-630561


CERTIFICATE OF COMPLETION

This certificate is proudly presented to

Mr. AJAY KUMAR

Has Successfully Completed the Value Added Certified
Course on TESSOLVE SEMICONDUCTORS IC
TESTING During 08.04.2019 to 12.04.2019


Mrs. R. RAJALAKSHMI A/ECE
HOD/ECE


DR R. RAJA
PRINCIPAL
PANDIAN SARASWATHI ENGINEERING COLLEGE

Digitally signed by
R RAJA
Date: 2024.06.27
10:40:38 +05'30'